

WF61 Datasheet

Amp'ed RF Technology, Inc.

WF61 Product Specification PRELIMINARY



12mm x 12mm x 2.2 mm (WF61A)
12mm x 12mm x 2.7 mm (WF61L)

Description

Amp'ed RF Tech presents the WF61 WiFi dual band module: 802.11abgn. The WF61 is a small footprint low cost RF module intended to help customers shorten product development cycles and reduce cost. It's integrated 2.4GHz and 5GHz PA provides ready to use dual band RF. It is fully compatible with Linux & Android systems or our own upper layer stack, Amp'ed UP, for a complete host & controller solution.

Features

WLAN

- 802.11a/b/g/n
- Dual Band: 2.4GHz and 5GHz
- Output Power, +21.5dBm for 2.4GHz; +16dBm for 5GHz
- Power savings mode
- Wi-Fi Direct (concurrent)
- Wi-Fi Protected Setup
- Soft Access Point
- Hotspot 2.0
- Security: WPAI/WPA2, AES, WEP

Hardware

- Interface, SDIO 2.0 or SPI
- 1 LPO input
- Standby current, 80 μ A
- RoHS conformance
- FCC/IC/CE certified (preliminary)

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1. Hardware Specifications

General Conditions (VIN= 3.6V and 25°C)

1.1. Recommended Operating Conditions

Rating	Min	Typical	Max	Unit
Operating Temperature Range	-40	-	85	°C
Supply Voltage VIN	2.3	3.6	4.8	Volts
Supply Voltage VDD_3V3	3.0	3.3	3.6	Volts
Supply Voltage VDD_1V8	1.65	1.8	1.95	Volts
Signal Pin Voltage	-	1.8	-	Volts
RF Frequency for 2.4G	2400	-	2483.5	MHz
RF Frequency for 5G	5150	-	5850	MHz

1.2. Absolute Maximum Ratings

Rating	Min	Typical	Max	Unit
Storage temperature range	-55	-	+150	°C
Supply voltage VIN	-0.3	-	+4.8	Volts
Supply Voltage VDD_3V3	-0.3	-	4.0	Volts
Supply Voltage VDD_1V8	-0.3	-	2.0	Volts
I/O pin voltage VIO	-0.3	-	+2.5	Volts
RF input power	-	-	-5	dBm

1.3. I/O Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IL}	Low-Level Input Voltage	-	0.6	Volts
V _{IH}	High-Level Input Voltage	1.3	-	Volts
V _{OL}	Low-Level Output Voltage	-	0.2	Volts
V _{OH}	High-Level Output Voltage	WF61A: 1.6 WF61L: 2.95	-	Volts
I _{OL}	Low –Level Output Current	-	4.0	mA
I _{OH}	High-Level Output Current	-	4.0	mA

1.4. Current Consumption

Modes (WLAN Max Power Consumption) VBAT=3.6v	Avg	Unit
Complete Power Down (PMUEN low)	17	μA
Sleep	101	μA
Power save (beacon period including DTIM 100ms, beacon length 1ms) -proprietary power saving features enabled	0.77	mA
RX (idle, 2.4GHz)	62.07	mA
RX (active, 2.4GHz, OFDM)	65.87	mA
TX (active, 2.4GHz, OFDM), 15.5dBm @RF port	257.67	mA
TX (active, 2.4GHz, OFDM), 20.5dBm @RF port	279.67	mA
Video streaming; the device is receiving 2.0 Mbps of data using legacy PSM mode (for example, MPEG-4@2Mbps)	13.31	mA
Ipeak: system maximum peak current draw	378	mA

1.5. Selected RF Characteristics

Parameters	Conditions	Typical	Unit
Antenna load		50	ohm
Wi-Fi Receiver 2.4GHz			
Sensitivity	DSSS 1Mbps@FER<8%	-98.5	dBm
Sensitivity	DSSS 2Mbps@FER<8%	-96	dBm
Sensitivity	CCK 5.5Mbps@FER<8%	-94	dBm
Sensitivity	CCK 11Mbps@FER<8%	-89	dBm
Sensitivity	BPSK 6Mbps@PER<10%	-94.5	dBm
Sensitivity	BPSK 9Mbps@PER<10%	-92.5	dBm
Sensitivity	QPSK 12Mbps@PER<10%	-91.5	dBm
Sensitivity	QPSK 18Mbps@PER<10%	-89	dBm
Sensitivity	16QAM 24Mbps@PER<10%	-86	dBm
Sensitivity	16QAM 36Mbps@PER<10%	-83	dBm
Sensitivity	64QAM 48Mbps@PER<10%	-78.5	dBm
Sensitivity	64QAM 54Mbps@PER<10%	-77	dBm
Sensitivity	BPSK 6.5Mbps@PER<10%	-92.5	dBm
Sensitivity	QPSK 13Mbps@PER<10%	-89.5	dBm
Sensitivity	QPSK 19.5Mbps@PER<10%	-87	dBm
Sensitivity	16QAM 26Mbps@PER<10%	-84.5	dBm
Sensitivity	16 QAM 39Mbps@PER<10%	-81	dBm
Sensitivity	64QAM 52Mbps@PER<10%	-77	dBm
Sensitivity	64QAM 58.5Mbps@PER<10%	-75.5	dBm
Sensitivity	64QAM 65Mbps@PER<10%	-73.5	dBm
Wi-Fi Receiver 5GHz 11n			
Sensitivity	BPSK 6.5Mbps@PER<10%,Nss=1	-91	dBm
Sensitivity	QPSK 13Mbps@PER<10%, Nss=1	-88	dBm
Sensitivity	16QAM 26MbpsPER<10%,Nss=1	-83	dBm
Sensitivity	64QAM 65MbpsPER<10%,Nss=1	-72.5	dBm
Wi-Fi Transmitter 2.4GHz			
Output Power	802.11b/g BPSK 1/2	20	dBm
Output Power	802.11b/g BPSK 3/4	20	dBm
Output Power	802.11b/g QPSK 1/2	20	dBm
Output Power	802.11b/g QPSK 3/4	20	dBm
Output Power	802.11b/g 16QAM 1/2	19.5	dBm
Output Power	802.11b/g 16QAM 3/4	18.5	dBm
Output Power	802.11b/g 64QAM 2/3	17.5	dBm
Output Power	802.11b/g 64QAM 3/4	16.5	dBm

Output Power	802.11n MCS-0	20	dBm
Output Power	802.11n MCS-1	20	dBm
Output Power	802.11n MCS-2	20	dBm
Output Power	802.11n MCS-3	19.5	dBm
Output Power	802.11n MCS-4	18.5	dBm
Output Power	802.11n MCS-5	17.5	dBm
Output Power	802.11n MCS-6	16.5	dBm
Output Power	802.11n MCS-7	15.5	dBm
Wi-Fi Transmitter 5GHz, 11n			
Output Power	802.11n MCS-7	16	dBm

1.6. Transmit Power Back-off

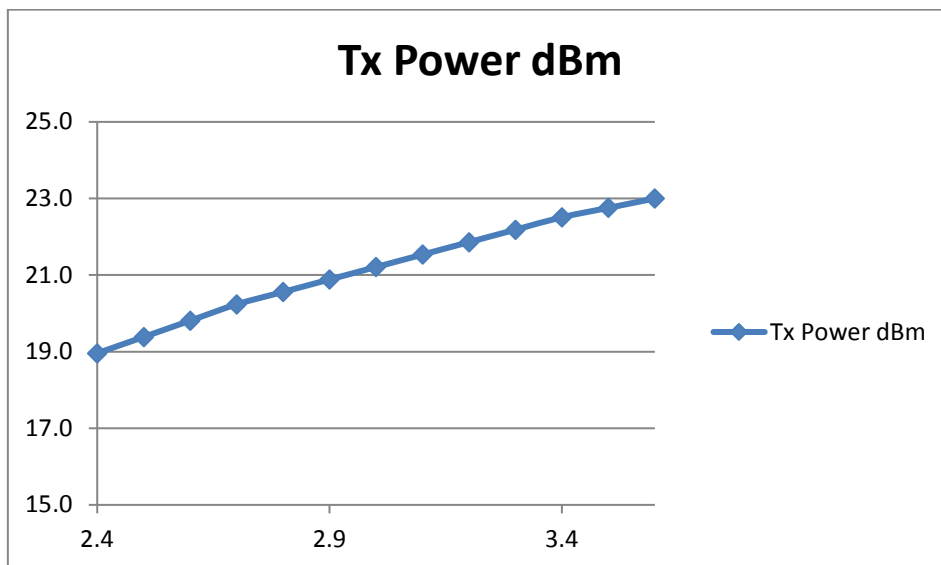
Note: Applies to 2.4GHz low band only.

When $2.7\text{ V} < V_{IN} < 3.6\text{ V}$, the output power shall be backed off by 0.8 dB per 300 mV drop of V_{IN} below 3.6 V to guarantee meeting spectral mask, EVM, harmonic levels, spurious emissions and regulatory requirements in general.

When $2.3\text{ V} < V_{IN} < 2.7\text{ V}$, an additional back-off of the output power is needed to guarantee meeting spectral mask, EVM, harmonic levels, spurious emissions and regulatory requirements in general.

When the load is not 50Ω , the output power is backed off to guarantee meeting spectral mask, EVM, harmonic levels, spurious emissions and regulatory requirements in general. The device is able to withstand a VSWR of up to 12:1 without any damage.

When the temperature increases from $+25^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, the output power shall be backed off by 0.25 dB per 10°C increase to guarantee meeting spectral mask, EVM, harmonic levels, spurious emissions and regulatory requirements in general.



Tx Power vs Vin

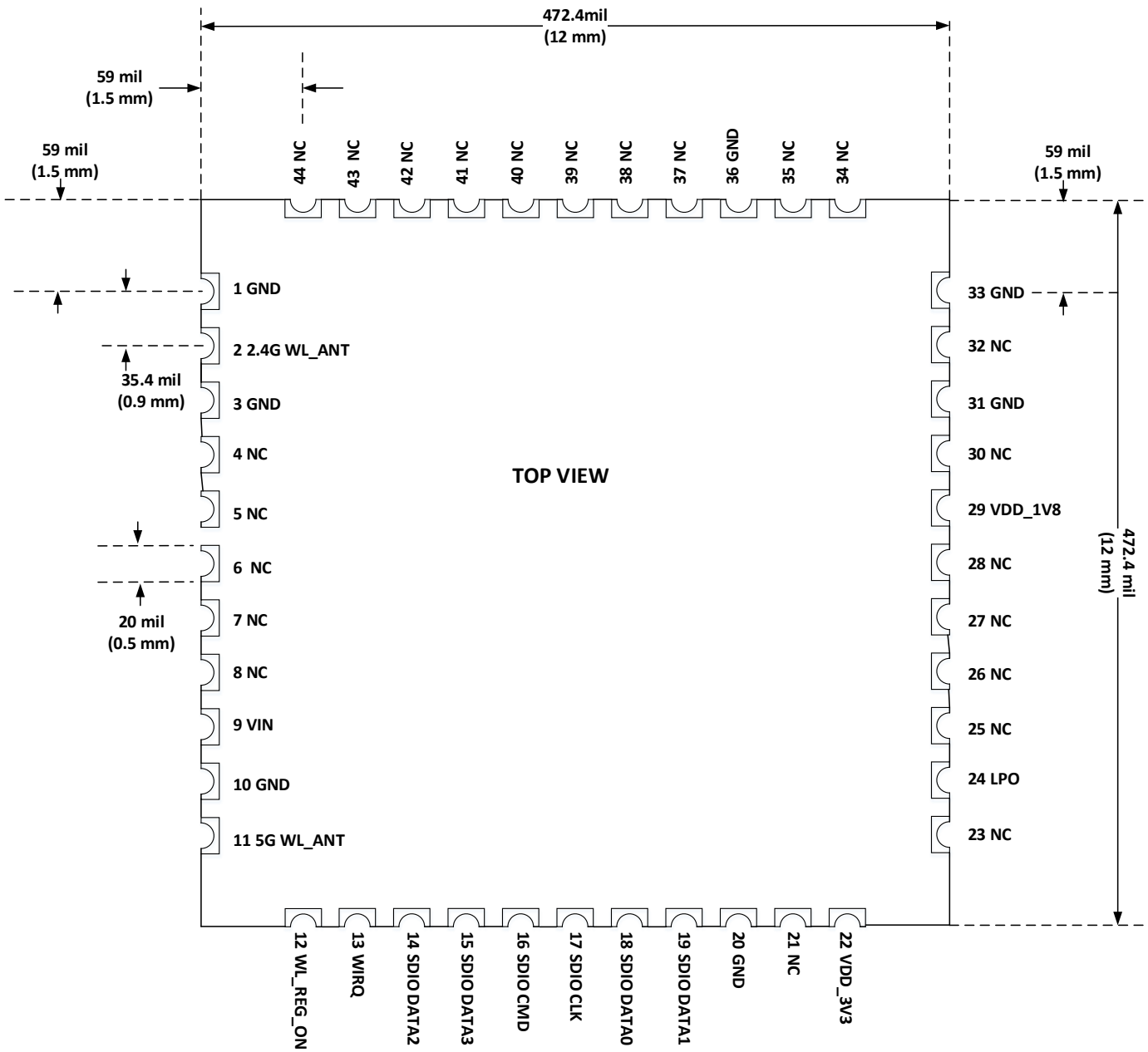
1.7. Pin Assignment

Name	Type	Pin #	Description	ALT Function	Logic Level WF61A	Logic Level WF61L
GND		1	Ground connections			
2.4G WL_ANT	I/O	2	Antenna port for 2.4G WIFI			
GND		3	Ground connections			
NC						
NC		5	Floating (Don't connected to ground)			
NC		6	Floating (Don't connected to ground)			
NC		7	Floating (Don't connected to ground)			
NC		8	Floating (Don't connected to ground)			
VIN		9	Main power voltage source input			
GND		10	Ground connections			
5 G WL_ANT	I/O	11	Antenna port for 5G WIFI			
WL_REG_ON	I	12	Internal regulators power enable/disable		1.8V	1.8V
WIRQ	O	13	SDIO external interrupt		1.8V	3.3V
SDIO DATA2	I/O	14	SDIO data line 2 / SDIO or SPI selection		1.8V	3.3V
SDIO DATA3	I/O	15	SDIO data line 3	SPI CSN	1.8V	3.3V
SDIO CMD	I/O	16	SDIO command line	SPI DI	1.8V	3.3V
SDIO CLK	I/O	17	SDIO clock line	SPI CLK	1.8V	3.3V
SDIO DATA0	I/O	18	SDIO data line 0	SPI DO	1.8V	3.3V
SDIO DATA1	I/O	19	SDIO data line 1	SPI INT	1.8V	3.3V
GND		20	Ground connections			
NC		21	Floating (Don't connected to ground)			
VDD_3V3		22	3.3V power supply input			
NC		23	Floating (Don't connected to ground)			
LPO	I	24	low power clock 32.768KHz		1.8V	1.8V
NC		25	Floating (Don't connected to ground)			
NC		26	Floating (Don't connected to ground)			
NC		27	Floating (Don't connected to ground)			
NC		28	Floating (Don't connected to ground)			
VDD_1V8		29	1.8V power supply input			
NC		30	Floating (Don't connected to ground)			
GND		31	Ground connections			
NC		32	Floating (Don't connected to ground)			

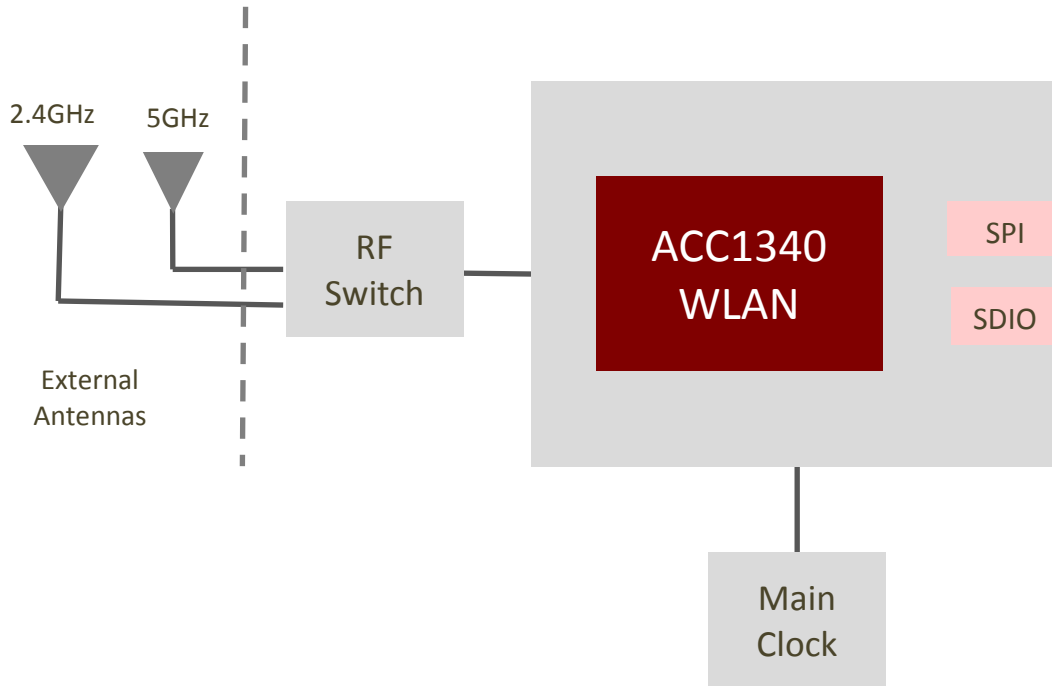
GND		33	Ground connections			
NC		34	Floating (Don't connected to ground)			
NC		35	Floating (Don't connected to ground)			
GND		36	Ground connections			
NC		37	Floating (Don't connected to ground)			
NC		38	Floating (Don't connected to ground)			
NC		39	Floating (Don't connected to ground)			
NC		40	Floating (Don't connected to ground)			
NC		43	Floating (Don't connected to ground)			
NC		42	Floating (Don't connected to ground)			
NC		41	Floating (Don't connected to ground)			
NC		44	Floating (Don't connected to ground)			

1.8. Layout Drawing

Size: 12 mm x 12 mm x 2.7 mm



2. Hardware Block Diagram



3. Hardware Design

3.1. PCB layout guide

- All unused pins should be left floating; do not ground.
- All GND pins must be well grounded.
- Traces should not be routed underneath the module.
- Use short, 50 ohm impedance, RF traces. Routing should be on the top layer.
- A diplexer may be used to combine 2.4GHz and 5GHz RF signals.
- Keep the clock and power signals far away from RF signals.
- Use two 22uF tan capacitors near Vin pin 9.
- SDIO signal traces should be equal length and their offset should be under ± 50 mil according to the clock of SDIO.
- All power supply lines should meet their power requirements.
- LPO traces should keep clear from other low frequency traces, such as IIC.

3.2. Module Reflow Installation

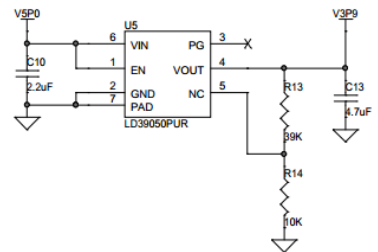
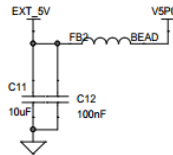
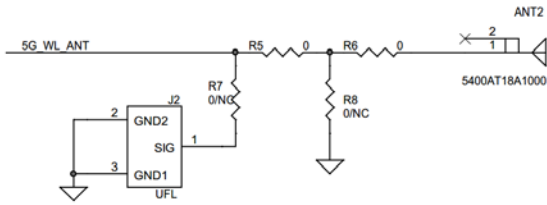
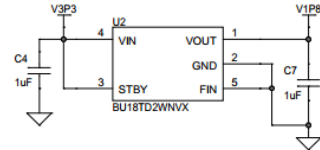
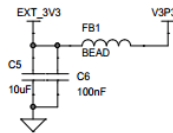
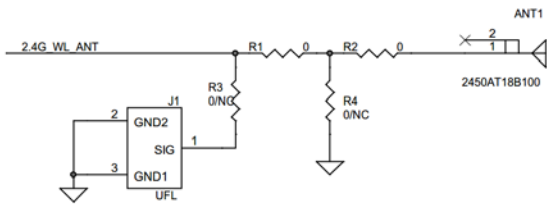
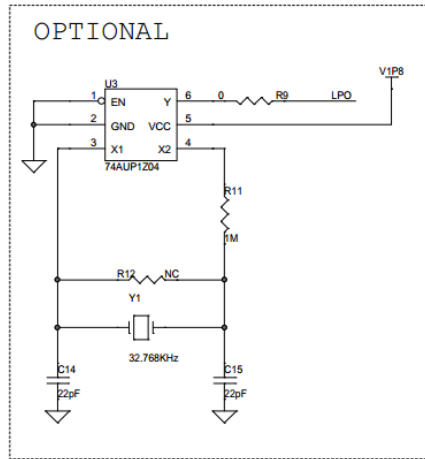
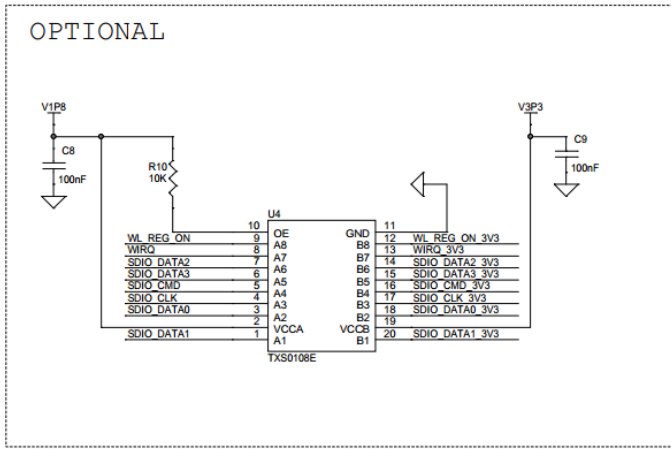
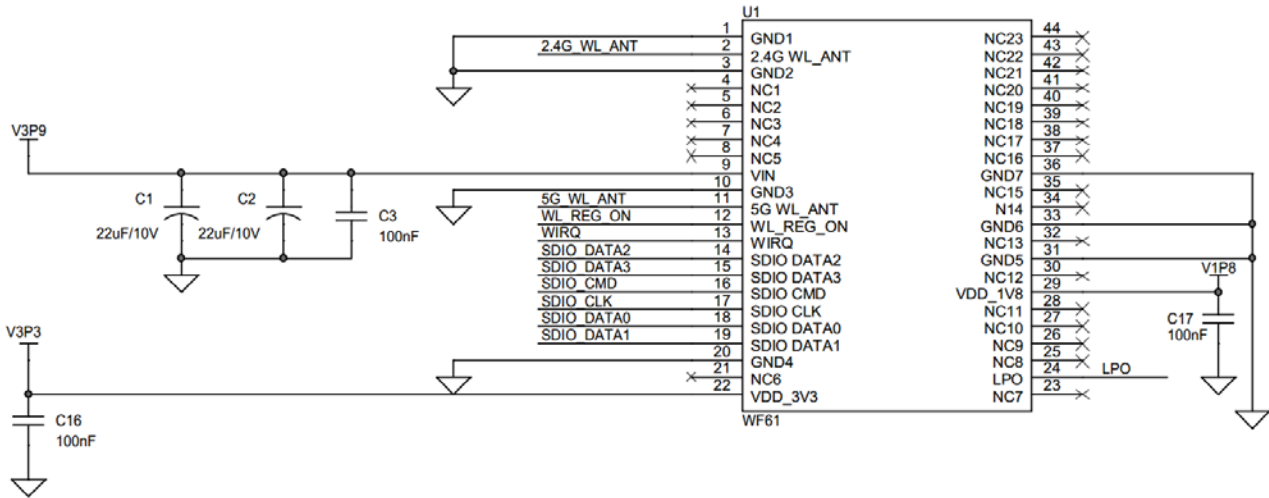
The WF61 is a surface mount WiFi module supplied on a 44 pin, 6-layer PCB. The final assembly recommended reflow profiles are:

For RoHS/Pb-free applications, Sn96.5/Ag3.0/Cu0.5 solder is recommended.

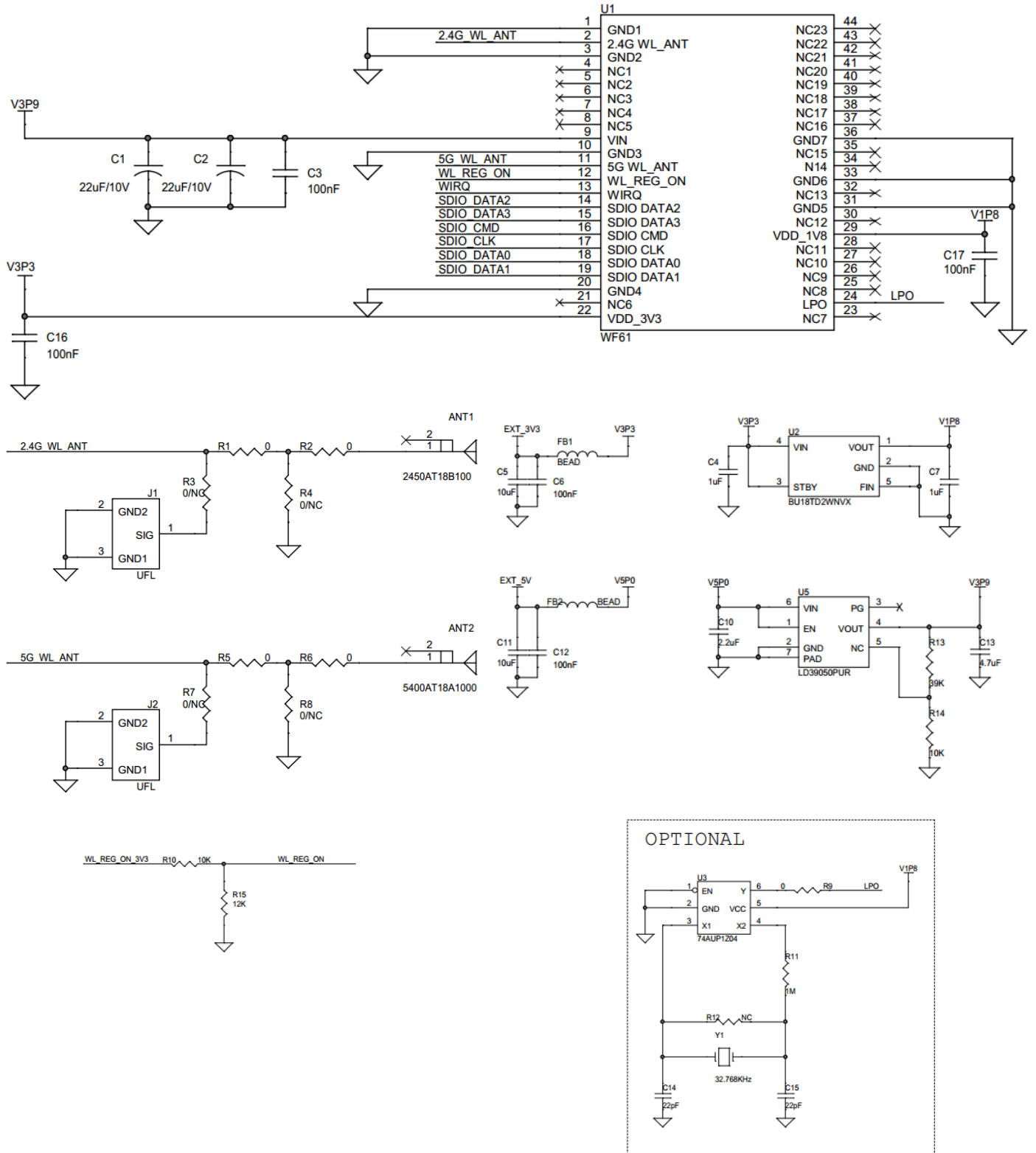
- Maximum peak temperature of 230° - 240°C (below 250°C).
- Maximum rise and fall slope after liquidous of < 2°C/second.
- Maximum rise and fall slope after liquidous of < 3°C/second.
- Maximum time at liquidous of 40 – 80 seconds.

4. Reference Design Example

The follow circuits are examples of typical Linux based platform connections to the WF61A module.



The follow circuits are examples of typical Linux based platform connections to the WF61L module.



5. Startup behavior

5.1. WL_REG_ON

A valid reset shall be obtained by maintaining WL_REG_ON active (low) for at least two cycles of LPO after VIN is stable within its operating range. There is no constraint on the activation of the other supplies during this process.

5.2. SDIO and SPI interface

SDIO DATA2 is the selection pin; the state of this pin is monitored on the rising edge of RESET.

- LOW selects SPI
- HIGH selects SDIO

6. Ordering Information

Part Name	Description
WF61A-DB	Dual band, external antenna 2.4GHz and 5GHz. 1.8V SDIO logic level. With no RF shielding use: WF61A-DB-NS.
WF61A-HB	High band, external antenna 5GHz. 1.8V SDIO logic level. With no RF shielding use: WF61A-HB-NS.
WF61A-LB	Low band, external antenna 2.4GHz. 1.8V SDIO logic level. With no RF shielding use: WF61A-LB-NS.
WF61L-DB	Dual band, external antenna 2.4GHz and 5GHz. 3.3V SDIO logic level, with RF shielding.
WF61L-HB	High band, external antenna 5GHz. 3.3V SDIO logic level, with RF shielding.
WF61L-LB	Low band, external antenna 2.4GHz. 3.3V SDIO logic level, with RF shielding.

7. Revision History

Date	Revision	Description
27-Apr-2017	1.0	Initial release
2-May-2017	1.1	Added level shifter option
8-May-2017	1.2	Update WF61L reference design
30-Jun-2017	1.3	Add PCB layout guide and add some PN
8-Sept-2017	1.4	Ordering part numbers updated.